

## PATENT APPLICATION

Sheet 1 of 1

FORM PTO-1449

LIST OF PATENTS AND PUBLICATIONS FOR  
APPLICANT'S INFORMATION DISCLOSURE  
STATEMENT

(Use several sheets if necessary)

ATTY. DOCKET NO.

10991915-1

APPLICATION NO.

09/499,720

CONFIRMATION NO.

APPLICANT

Dale C. Morris et al.

FILING DATE

Feb. 8, 2000

GROUP

## REFERENCE DESIGNATION

## U.S. PATENT DOCUMENTS

EXAMINER INITIAL		DOCUMENT NUMBER	PUBLICATION DATE	NAME	Pages, Columns, Lines Where Relevant Passages or Figures Appear
MI	1A	5,941,977	8/1999	Panwar et al.	
	1B				
	1C				
	1D				
	1E				
	1F				
	1G				
	1H				
	1I				
	1J				
	1K				

RECEIVED

FEB 19 2003

Technology Center 2100

## FOREIGN PATENT DOCUMENTS

		DOCUMENT NUMBER	PUBLICATION DATE	NAME OF PATENTEE OR APPLICANT	Pages/Columns/Lines Where Relevant Passages/Figures Appear	Check if Translation attached
MI	1L	WO 99/19795	22 Apr. 1999	Dale C. Morris et al		
	1M					
	1N					
	1O					
	1P					

## OTHER REFERENCES (including Author, Title, Date, Pertinent Pages, etc.)

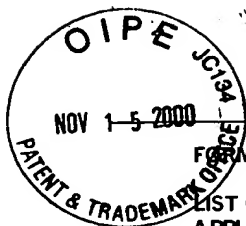
MI	1Q	David M. Gallagher et al., "Dynamic Memory Disambiguation Using the Memory Conflict Buffer," ACM Press, ASPLOS VI Proceedings, pp. 183-193 (Oct. 1994).
MI	1R	A copy of European Search Report for Application No. EP 01 30 3750 mailed on December 23, 2002 (3 pages).
	1S	

EXAMINER

midys Inoa

DATE CONSIDERED

4/2/04



FORM PTO-1449

LIST OF PATENTS AND PUBLICATIONS FOR  
APPLICANT'S INFORMATION DISCLOSURE  
STATEMENT

(Use several sheets if necessary)

ATTY. DOCKET NO.

10991915-1

SERIAL NO.

09/499,720

APPLICANT

Dale C. Morris et al.

FILING DATE

Feb. 8, 200

GROUP

## REFERENCE DESIGNATION

## U.S. PATENT DOCUMENTS

EXAMINER INITIAL		DOCUMENT NUMBER	DATE	NAME	CLASS	SUB CLASS
	1A					
	1B					
	1C					
	1D					
	1E					
	1F					
	1G					
	1H					
	1I					
	1J					
	1K					

RECEIVED  
NOV 21 11 AM  
Technology Center 2100

## FOREIGN PATENT DOCUMENTS

		DOCUMENT NUMBER	DATE	NAME	CLASS	SUB CLASS	TRANSLATION	
							YES	NO
	1L							
	1M							
	1N							
	1O							
	1P							

## OTHER REFERENCES (including Author, Title, Date, Pertinent Pages, etc.)

MI	1Q	Gerry Kane, "PA-RISC 2.0 Architecture," Prentice-Hall, Inc., Hewlett-Packard Professional Books, pp. iii-iv-10 (1996).
	1R	
	1S	

EXAMINER

Mudys Iroa

DATE CONSIDERED

4/2/04